



SRI VENKATESWARA COLLEGE OF ENGINEERING (Autonomous)

Karakambadi Road, Opposite LIC Training Centre, Tirupati – 517 507.
Accredited by NBA (B.Tech – CSE, ECE and EEE) & NAAC with ‘A’ Grade
Approved by AICTE, New Delhi permanently affiliated to JNTUA, Ananthapuram.

Faculty Development Programmes Organized by the faculty

S.NO	Name of the FDP	Resource Person(s)	Date	No. of Days
2019-20				
II-Sem				
1	ANN and its Applications	NITTTTR, Chandigarh (through ICT)	27-01-2020 to 31-01-2020	5 Days
I-Sem				
1	Time and mind management for guilt-free work-life balance	Ms.Lakshmi Mukkavilli Chairwoman,IWN AP And Managing Director, Patra India BPO Services Pvt. Ltd	20-09-2019	1 Day
2	Image Processing & Signal Processing Using SCI Lab	NITTTTR, Chandigarh (Through ICT)	05-08-2019 to 09-08-2019	5 Days
3	Creating Significant Teaching Learning & Research Experience	Dr. Ushaa Eswaran, Mr.Jayaraman, K.Iyer,Dr.Cyiril Prasanna Raj	01-07-2019 to 06-07-2019	1 Week



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S.NO	Name of the FDP	Resource Person(s)	Date	No. of Days
2020-21				
I-Sem				
1	FDP on eSim	In Association with Spoken Tutorial – IIT Bombay	22-06-2020 to 26-06-2020	5 Days
2	Arduino, NodeMCU and IoT	S.Lokesh, Research Scholar, ECE Dept., SVUCE, Tirupati.	27-07-2020 to 29-07-2020	3 Days
II-Sem				
1	Hands-On-Training on VLSI Circuit Design (Analog/Digital) Using Cadence Suite	1. Kedharnath L V, ASIC Engineer, Entuple Technologies Pvt. Ltd., Bangalore. 2. P.Santosh Kumar, Physical Design Engineer, Cadence Design Systems, Bangalore. 3. N. Sreenivasulu, SoC Design Engineer, Cadence Design Systems, Bangalore	22-04-2021 to 24-04-2021	5 Days



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Department of Electronics and Communication Engineering

Faculty Development Programmes Organized by the faculty

S.NO	Name of the FDP	Resource Person(s)	Date	No. of Days
2021-22				
I-Sem				
1	Application of Embedded Systems to Internet of Things (AESIoT)	K. S. Kavin, Founder and Managing Director, AB Technologies, Chennai	20/09/2021 to 24/09/2021	5 Days
II-Sem				
1	Hands-On-Training on VLSI Circuit Design (Analog/Digital) Using Cadence Suite	1. Kedharnath L V, ASIC Engineer, Entuple Technologies Pvt. Ltd., Bangalore. 2. P.Santosh Kumar, Physical Design Engineer, Cadence Design Systems, Bangalore. 3. N. Sreenivasulu, SoC Design Engineer, Cadence Design Systems, Bangalore	07-03-2022 to 11-03-2022	5 Days