

6th Meeting of Board of Studies (BOS) in Computer Science and Engineering

5th July 2025

Minutes of Meeting



DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

SRI VENKATESWARA COLLEGE OF ENGINEERING

(AUTONOMOUS)

(Affiliated to J.N.T. University Anantapur, Ananthapuramu), Accredited by NBA & NAAC “A”

Karakambadi Road, Tirupati - 517 507



**Board of Studies in Computer Science and Engineering
(2024-25 to 2026-27)**

S. No.	Name	Designation and Address	Role
1	Dr. A. Ganesh	Professor & HOD Department of CSE SV College of Engineering, Tirupati Mobile No. 9160250264 E-Mail ID: hod_cse@svce.edu.in	Chairman
2	Prof. S. Viswanadha Raju	Professor of CSE Jawaharlal Nehru Technological University Anantapur Mobile: +91-9963701506 Email : svraju.jntu@gmail.com	External Member nominated by JNTUA (Academia)
3	Dr. U. S. N. Raju	Professor of CSE NIT, Warangal. Mobile: 9440149146 Email: usnraju@nitw.ac.in	External Member (Academia)
4	Dr. P. Venkata Krishna	Professor, Dept. of CSE, SPMVV, Tirupathi. Mobile: +91-9443215749 Email: parimalavk@gmail.com	External Member (Academia)
5	Ms. Venkata Rajitha	Technology Lead Infosys Private Limited Phase I, Electronic city, Bangalore,560100 Ph. No.: 7893165509 Email: rajithaharsha9@gmail.com	External Member (Industry)
6	Ms. M. Jayasree	Systems Engineer TCS, Bangalore, Ph. No.: 6302660086 Email: jayasreereddym27@gmail.com	External Member (Alumni)
7	Dr. Y.A Siva Prasad	Professor Department of CSE SV College of Engineering, Tirupati Ph. No.:9966766320 Email: sivaprasad.y@svcolleges.edu.in	Internal Member (Academia)
8	Dr. M. Vikram	Professor Department of CSE SV College of Engineering, Tirupati Ph. No 9440920836	Internal Member (Academia)

		Email: vikram.m@svcolleges.edu.in	
9	Dr. T Sree Lakshmi	Associate Professor Department of CSE SV College of Engineering, Tirupati Ph. No.: 9966768498 Email: sreelakshmi.t@svce.edu.in	Internal Member (Academia)
10	Dr. S Sajida	Associate Professor Department of CSE SV College of Engineering, Tirupati Ph. No.: 7893337513 Email: sajida.s@svce.edu.in	Internal Member (Academia)
11	Dr. G T Prasanna Kumari	Associate Professor Department of CSE SV College of Engineering, Tirupati Ph. No.: 9440410765 Email: prasannakumari.g@svcolleges.edu.in	Internal Member (Academia)
12	Dr. A. Saritha	Associate Professor Department of CSE SV College of Engineering, Tirupati Ph. No.: 9490621155 Email: saritha.a@svcolleges.edu.in	Internal Member (Academia)
13	Dr.M. Bhargavi Krishna	Assistant Professor Department of CSE SV College of Engineering, Tirupati Ph. No. 7989100279 Email: bhargavikrishna.m@svce.edu.in	Internal Member (Academia)
14	Dr.P. Prathusha	Associate Professor Department of CSE SV College of Engineering, Tirupati Ph. No. 8919196848 Email: prathusha.p@svce.edu.in	Internal Member (Academia)
15	Mr. Mohammad Rafi S	Assistant Professor Department of Computer Science & Engineering SV College of Engineering, Tirupati Ph. No.: 9010766896 Email: rafi.md@svcolleges.edu.in	Internal Member (Academia)
16	Mr. J. Shankar Babu	Assistant Professor Department of Computer Science & Engineering SV College of Engineering, Tirupati Ph. No.: 6300645303 Email: shankar.j@svcolleges.edu.in	Internal Member (Academia)
17	Ms. P. Lakshmi	Assistant Professor Department of Computer Science & Engineering SV College of Engineering, Tirupati Ph. No.: 9849529517 Email: lakshmi.p1@svcolleges.edu.in	Internal Member (Academia)



6th Meeting of Board of Studies in Computer Science and Engineering
on 05.07.2025 at 11:00 AM

(Online Mode through Google Meet Platform)

Meeting Link: <https://meet.google.com/zxf-vmwp-owk>

AGENDA

1. Welcome address by BOS chairman to all the BOS Members.
2. Action Taken Report on Minutes/Resolutions of 5th BOS Meeting
3. Report on the Implementation of Autonomy with Respect to Course Structure, Syllabi & Academic Regulations for II B.Tech. Computer Science and Engineering I and II Semesters (Under SVCE R23 Regulations)
4. Course Structure and Syllabi for the following Courses of III and IV B.Tech. Computer Science and Engineering I and II Semesters (Under SVCE R23 Regulations)

III B.Tech. I Semester:

- Automata Theory and Compiler Design
- Data Warehousing and Data Mining
- Data Warehousing and Data Mining Lab
- Full Stack Development- II

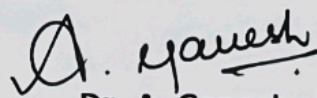
III B.Tech. II Semester:

- Distributed Operating System
- Software Project Management
- Technical Paper Writing and IPR
- DevOps
- Introduction to Quantum Technology and Applications

IV B.Tech. I Semester:

- Agile Methodologies
- Software Architecture and Design Pattern
- Computer Vision

5. Syllabi for the Courses offered to Other Departments (Under SVCE R23 Regulations)
 - Computational Intelligence (III-I, CSM)
 - Python Programming for Engineers (III-I, CE)
 - Quantum Computing (III-II, CSD, CSM, IT)
 - Agile Software Development (IV-I, CSD)
 - Principles of Operating Systems (Open Elective-I)
 - Fundamentals of Object-Oriented Analysis & Design (Open Elective-II)
 - Java Programming (Open Elective-II)
 - Introduction to Data Base Management Systems (Open Elective-III and IV)
6. Course Structure and Syllabi for the Courses of Honors Degree offered by Department of Computer Science and Engineering (Under SVCE R23 Regulations)
7. Course Structures and Syllabi for the Courses of Minor Degrees offered by Department of Computer Science and Engineering (Under SVCE R23 Regulations)
8. List of Courses in which the Content of the Syllabus is changed more than 20% in SVCE R23 Regulations in comparison with SVCE R20 Regulations.
9. List of New Courses introduced in SVCE R23 Regulations in comparison with SVCE R20 Regulations
10. Identification of MOOCs Equivalent to the Courses offered under UG (including Honors/Minors) and PG Curriculum.
11. Panel of Examiners for Question Paper Setting for the Courses of III and IV B.Tech. Computer Science and Engineering I and II Semesters, Courses of Minors / Honors and Courses offered to Other Departments (Under SVCE R23 Regulations).
12. Substitute Subjects
13. Suggestions from BOS Members for the Development of Department/College



Dr. A. Ganesh

Chairman

BOS in Computer Science and Engineering

HEAD OF THE DEPARTMENT
COMPUTER SCIENCE AND ENGINEERING
SV COLLEGE OF ENGINEERING
KARAKAMBADI ROAD TIRUPATI-517507



6th Meeting of Board of Studies in Computer Science and Engineering

on 10.07.2025 at 3:00 PM

MINUTES OF MEETING

Members Present:

S. No.	Name	Designation and Address	Role
1	Dr. A. Ganesh	Professor & HOD Department of CSE SV College of Engineering, Tirupati Mobile No. 9160250264 E-Mail ID: hod_cse@svce.edu.in	Chairman
2	Prof. S. Viswanadha Raju	Professor of CSE Jawaharlal Nehru Technological University Anantapur Mobile: +91-9963701506 Email : svraju.jntu@gmail.com	External Member nominated by JNTUA (Academia)
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17	Ms. P. Lakshmi	Assistant Professor Department of CSE SV College of Engineering, Tirupati Ph. No.: 9849529517 Email: lakshmi.p1@svcolleges.edu.in	Internal Member (Academia)

Screenshots of the Online Meeting:

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11:51 | zxf-vmwp-owk

webinar svce (Presenting, annotating)

CSE - B. Tech III Year I Semester

JNTUA

III B.Tech Semester (CSE)

S.No	Course Code	Title	L	T	P	Credits
1	23A05501T	Artificial Intelligence	3	0	0	3
2	23A05502T	Computer Networks	3	0	0	3
3	23A05503	Automata Theory and Compiler Design	3	0	0	3
4	23A05504a	Professional Elective-I	3	0	0	3
	23A05504b	1. Object Oriented Analysis and Design				
	23A05504c	2. Self Computing				
	23A05504d	3. Microprocessors and Microcontrollers				
	23A05504e	4. Data Warehousing and Data Mining				
5	23A05501P	Open Elective-I	3	0	0	3
6	23A05502P	Artificial Intelligence Lab	0	0	3	1.5
7	23A05503P	Computer Networks Lab	0	0	3	1.5
8	23A05506	Skill Enhancement Course - Full Stack Development - II	0	1	2	2
9		Training Lab	0	0	2	1
10		Evaluation of Community Service Internship	-	-	-	2
Total			15	1	10	25

12weekMOOCsfromNPTELcoursesrecommendedforBoS

SVCE

S. No	Course Code	Course Name	Category	Contact Periods per Week			Credits
				L	T	P	
1	CS23APCS01	Automata Theory and Compiler Design	PC	3	-	-	3
2	CY23APCS01	Computer Networks	PC	3	-	-	3
3	CS23APES02	Data Warehousing and Data Mining	PC	3	-	-	3
4	IT23APCS01	Professional Elective-I	PE	3	-	-	3
	EC23APCS04	Software Engineering					
	IT23APES02	Microprocessors and Microcontrollers					
	AM23APES01	Object Oriented Analysis and Design					
	AM23APES01	Self Computing					
5		Open Elective-I	OE	3	-	-	3
6	CY23APCS02	Computer Networks Lab	PC	-	-	3	1.5
7	CS23APCS03	Data Warehousing and Data Mining Lab	PC	-	-	3	1.5
8	CS23APCS01	Full Stack Development-II	SC	-	1	2	2
9	CS23APCS01	Domain Specific Expert Lecture	AC	-	-	-	-
10	CS23APCS02	Domain Specific Workshop	AC	-	-	-	-
11	CS23APCS01	Community Service Project Internship	CS	-	-	-	2
TOTAL				15	1	10	25

BoS Meeting Dept. of Computer Science and Engineering

11:07 | zxf-vmwp-owk

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Subjects to discuss.pdf

1. Demonstrate knowledge on Automata Theory, Regular Expression and Analyze and Design of finite automata, and prove equivalence of various finite automata.
2. Demonstrate knowledge on context free grammar, Analyze and design of PDA and TM.
3. Understand the basic concept of compiler design, and its different phases which will be helpful to construct new tools like LEX, YACC, etc.
4. Ability to implement semantic rules into a parser that performs attribution while parsing and apply error detection and correction methods.
5. Apply the code optimization techniques to improve the space and time complexity of programs while programming and Ability to design a compiler.

UNIT-6
Introduction to Automata and Regular Expressions
Introduction: Alphabets, Strings and Languages, Chomsky Hierarchy, Automata and Grammars, Regular Grammar and Languages, Finite Automata, Deterministic Finite Automata (DFA), Non-deterministic Finite Automata (NFA), Equivalence of NFA and DFA, Minimization of Finite Automata, Regular Expressions, Converting Regular Grammar and Expression into Finite Automata, Pumping lemma for regular sets, Closure properties of regular sets (Without proof).

UNIT-7
Context Free Grammars and Pushdown Automata
Context Free Language, Context Free Grammar, Derivation and Parse tree, Ambiguity, Specification of CFLs, Chomsky Normal Form, Greibach Normal Form, Push Down Automata (PDA), Design of PDA, Equivalence of PDA and CFL/CFG.

UNIT-8
Turing Machines and Introduction to Compilers
Turing Machine, TM Model, Language acceptance, Design of Turing Machine, Compilers, Phases of Compiler, The role of Lexical Analyzer, Input Buffering.

11:14 | zxf-vmwp-owk

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Lecture

S. No	Course Code	CourseName	Category	Contact Periods per Week	Credits	Scheme of Examination
				L T P Total		Int. Marks Ext. Marks Total Marks
1	CS23APW801	Internship	PW	- - - 4	-	- - -
2	CS23APW802	Project	PW	- - - 8	60	140 200
Total				- - - 12	60	140 200

B. Tech. – IV Year II Semester

S. No	Course Code	CourseName	Category	Contact Periods per Week	Credits	Scheme of Examination
				L T P Total		Int. Marks Ext. Marks Total Marks
1	CS23APW801	Internship	PW	- - - 4	-	- - -
2	CS23APW802	Project	PW	- - - 8	60	140 200
Total				- - - 12	60	140 200

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viswanadharaju somalaraju

Divya Nagolu

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Contributors 13

Divya Nagolu (You)

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Chandrakala Madhirala

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U.S.N. Raju

Divya Nagolu

9 others

12:02 | zxf-vmwp-owk

05-07-2025

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11 Lecture

Domain Specific Workshop AC

Total 18 2 2 22 21 210 590 800

B. Tech. - IV Year II Semester

S. No	Course Code	CourseName	Category	Contact Periods per Week				Credits	Scheme of Examination Max. Marks		
				L	T	P	Total		Int. Marks	Ext. Marks	Total Marks
1	CS23APW801	Internship	PW	-	-	-	4	-	-	-	
2	CS23APW802	Project	PW	-	-	-	8	60	140	200	
Total				-	-	-	12	60	140	200	

1. Welcome and Introduction of BOS Members:

BOS Chairman welcomed and introduced all the members of BOS.

2. Action Taken Report on “Minutes/Resolutions of 5th BOS Meeting held on 7-08-2024:

Minutes of 5th BOS Meeting held on 7-08-2024	Action Taken
II Year B. Tech. Computer Science and Engineering Course Structure & Syllabi (Under SVCE R23 Regulations)	Implemented for II B.Tech. Computer Science and Engineering I& II Semesters from the academic year 2024-25 (Under SVCE R23 Regulations).
Panel of Examiners for Question Paper Setting	Panels were submitted to the Chairman, Academic Council for the approved courses of II B.Tech. I and II Semesters (Under SVCE R23 Regulations) during the academic year 2024-25.

The above Action Taken Report is noted for record.

3. Report on Implementation of Autonomy with respect to Course Structure, Syllabi and Academic Regulations for II B.Tech. Computer Science and Engineering I and II Semesters (Under SVCE R23 Regulations):

BOS Chairman presented a report on implementation of autonomy to the members. The board has congratulated the college authorities for successful implementation of R23 regulations and syllabi for II B.Tech. CSE I and II semesters **(ANNEXURE-I)**.

4. Course Structure and Syllabi for the Courses of III and IV B.Tech. Computer Science and Engineering I and II Semesters (Under SVCE R23 Regulations):

BOS Chairman presented the proposed course structure and syllabi of 16 core courses of III and IV B. Tech. Computer Science and Engineering I and II Semesters to the BOS members and sought suggestions **(ANNEXURE-II)**. Members appraised the Syllabi of other courses in B.Tech. Computer Science and Engineering. BOS Members made the following suggestions for further improvement and approved the course structure and syllabi. Members have appreciated the efforts of Chairman and Internal Members of BOS and the Department in framing course structure and syllabi in a best possible way.

- i. Include GATE-related topics in the course "Automata Theory and Compiler Design."
- ii. Add a new course on “Introduction to Quantum Technologies and Applications “for all branches in III B. Tech. I or II Semester.
- iii. For laboratory courses, the programs should focus on application-oriented learning.
- iv. It is recommended to design lab programs for all labs by including at least one or two case studies.
- v. For all Professional Elective courses, consider utilizing one hour per week for practical sessions, depending on the subject.

- vi. Offer at least one Course through MOOCs via SWAYAM NPTEL
- vii. All theory courses should incorporate one or two real-world applications.
- viii. A minimum level of practical knowledge is mandatory for open elective courses.
- ix. In some courses, the organization of topics needs to be improved.
- x. While writing course outcomes, ensure they are written as clear statements, not in the form of questions.
- xi. Include a prerequisite chart for the entire course structure.
- xii. Introduce industry-aligned certifications such as AWS, Azure, Qis kit, and Tensor Flow as part of elective or lab components.
- xiii. Group electives into interdisciplinary modular tracks like AI/ML, Cybersecurity, and DevOps for better career orientation.
- xiv. Add emerging topics such as Generative AI, Edge Computing, and Digital Twins as professional electives in final year.
- xv. Merge lab exercises with lecture topics weekly for hands-on learning rather than isolating labs to separate courses.
- xvi. Allow audit courses like Technical Paper Writing & IPR to be converted into credit-based electives with proper assessment.
- xvii. Include additional skill- based electives like UI/UX Design, System Design, and API Development under Skill Enhancement Courses.
- xviii. Offer simplified CSE open electives for other departments with bridging modules to encourage interdisciplinary enrolment.
- xix. Redesign some final-year electives to be project-based, allowing students to implement real- world solutions with industry mentorship.
- xx. Launch short-term floating electives or micro-courses each semester on trending technologies voted by students or faculty.
- xxi. Scale up employability training by integrating communication skills, technical mock interviews, and portfolio development into the curriculum
- xxii. Including topics on the applications of Artificial Intelligence, Cyber security and Data science in Computer Science and Engineering.
- xxiii. All theory courses should incorporate one or two real-world applications
- xxiv. BOS member suggested that the department should focus on Practical orientation of theory subjects and suggested a format for Laboratory subjects which is as follows:

1.Traveling Salesman Problem:

- b. **Experiment:** Solve TSP using search algorithms
- c. **Real Example:** Delivery Route Optimization
- d. **Use Case:** Logistics, route planning, maps.

2.Simulated Annealing:

- e. **Experiment:** Implement simulated annealing to solve optimization problems
- f. **Real Example:** CPU Load Balancing
- g. **Use Case:** Scheduling, resource optimization

5. Syllabi for the Courses offered to Other Departments (Under SVCE R23 Regulations)

BOS Chairman presented the proposed course syllabi for the courses (III and IV B.Tech. I

and II Semesters) (Under SVCE R23 Regulations) offered to other departments, to the BOS members and sought suggestions (**ANNEXURE-III**) and members approved the same.

6. Course Structure and Syllabi for the Courses of Honors Degree offered by Department of Computer Science and Engineering (Under SVCE R23 Regulations)

BOS Chairman presented the proposed course structure and syllabi for the courses of Honors Degree in Computer Science and Engineering to the BOS members and sought suggestions (**ANNEXURE - IV**) and members approved the same.

7. Course Structures and Syllabi for the Courses of Minor Degrees offered by Department of Computer Science and Engineering (Under SVCE R23 Regulations)

BOS Chairman presented the proposed course structures and syllabi for the courses of Minor Degrees in Computer Science and Engineering to the BOS members and sought suggestions (**ANNEXURE –V**). BOS members approved and suggested the integration of SWAYAM NPTEL for minor courses.

8. List of Courses in which the Content of the Syllabus is changed more than 20% in SVCE R23 Regulations in comparison with SVCE R20 Regulations

BOS Chairman presented the list of courses in which the content of the syllabus is changed more than 20% in SVCE R23 Regulations in comparison with SVCE R20 Regulations, to the BOS members for information (**ANNEXURE - VI**). BOS Members noted the same.

9. List of New Courses introduced in SVCE R23 Regulations in comparison with SVCE R20 Regulations

BOS Chairman presented the list of new courses introduced in SVCE R23 Regulations in comparison with SVCE R20 Regulations, to the BOS members for information (**ANNEXURE - VII**). BOS Members noted the same.

10. Identification of MOOCs Equivalent to the Courses offered under UG (including Honors/Minors) and PG Curriculum

Resolved to authorize the BOS Chairman in Computer Science and Engineering to finalize the MOOCs Equivalent to the Courses offered under UG (including Honors/Minors) and PG Curriculum as and when required and forward the same to the Chairman, Academic Council for approval.

11. Panel of Examiners for Question Paper Setting for the Approved Courses of III and IV B. Tech. Computer Science and Engineering I and II Semesters and Courses of Minors /Honors and Courses offered to Other Departments (Under SVCE R23 Regulations).

Members resolved to authorize the BOS Chairman in Computer Science and Engineering to prepare the Panel of Examiners for Question Paper Setting for core theory courses of III and IV B.Tech. CSE I and II Semesters, Courses of Minors / Honors and courses offered to other departments (Under SVCE R23 Regulations) and furnish to the Chairman, Academic Council for further action within fifteen days after the commencement of the semester.

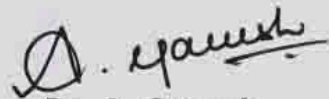
12. Substitute Subjects

Resolved to authorize the BOS Chairman in Computer Science and Engineering to finalize the substitute subjects as and when required and forward the same to the Chairman, Academic Council for further action.

13. Suggestions from BOS Members for the Development of Department/College:

The department should focus on enhancing the industry readiness of students by:

- i. Providing Hands-on training,
- ii. Strengthen technical skills of students
- iii. Conduct more workshops and training programs on the latest software and modern technologies.



Dr. A. Ganesh

Chairman

BOS in Computer Science and Engineering

HEAD OF THE DEPARTMENT

COMPUTER SCIENCE AND ENGINEERING

SV COLLEGE OF ENGINEERING

KARAKAMBADI ROAD TIRUPATI-517507



Department of Computer Science and Engineering

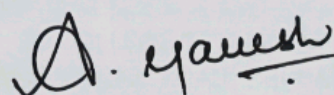
Report on Implementation of Autonomy with Respect to Syllabi & Academic Regulations for II B. Tech. Computer Science and Engineering I and II Semesters (Under SVCE R23 Regulations)

II B. Tech. I Semester (SVCE R23 Regulations)

Name of the course	Improvements/Modifications required if any
Probability and Statistics	Integrate real-world applications and datasets using Python, R, and Excel, covering topics like Monte Carlo simulations, bootstrapping, and Bayesian inference. Emphasize practical assignments, data visualization, collaborative learning, ethics, and diverse, hands-on assessments.
Digital Logic and Computer Organization	Modernize the course by incorporating hands-on projects with tools like Verilog or VHDL and interactive labs focused on hardware design. Emphasize real-world applications, current processor organization topics, and collaborative, design-oriented activities to enhance system implementation skills.
Organizational Behavior	The course should incorporate practical examples from CSE and IT workplace scenarios. Interactive activities such as role-plays, case studies, and behavioral assessments—supported by tools like Mentimeter and Padlet—can boost engagement and deepen real-world understanding.
Advanced Data Structures and Algorithm Analysis	The course should incorporate contemporary topics such as remote work, diversity, and emotional intelligence using real-world cases and interactive activities. Use technology for collaboration and encourage reflective assignments to develop self-awareness and leadership skills.
Object Oriented Programming Through JAVA	Emphasize practical Java coding assignments and projects incorporating design patterns, exception handling, and file I/O. Include hands-on work with relevant libraries, collaborative exercises, code reviews, and IDE use for real-world development skills.
Design Thinking and Innovation	The course can be enriched by incorporating real-world tech case studies and startup success stories. Hands-on activities like prototyping, user journey mapping, and collaborative brainstorming using tools like Miro or Canva can enhance creativity and problem-solving skills.
Object Oriented Programming Through JAVA Lab	The lab experience can be enriched by incorporating real-world problem statements such as file handling systems, student record management, and basic e-commerce applications. Utilizing modern IDEs like Eclipse or IntelliJ, along with the inclusion of mini-projects, can significantly enhance hands-on learning and practical application.
Python Programming	The course can incorporate real-world tasks such as web scraping, automation, and data analysis, providing practical exposure through tools like Jupyter Notebook, Google Colab, Pandas, and Matplotlib.
Advanced Data Structures and Algorithm Analysis Lab	The number of programs should be streamlined to allow greater emphasis on testing, debugging, performance analysis, and the practical application of algorithms.

II B. Tech. II Semester (SVCE R23 Regulations)

Name of the course	Improvements/Modifications required if any
Discrete Mathematics and Graph Theory	Incorporate practical applications of discrete structures and graph theory across diverse domains, utilizing tools like Python or MATLAB for problem-solving. Use case studies or mini-projects to enhance understanding and increase real-world relevance.
Universal Human Values	The course should link human values with CSE through relevant examples. Clear outcomes and reflective assessments in practice sessions will enhance learning. Adding Sustainable Development Goals and professional ethics will make the syllabus more industry- relevant.
Database Management Systems	The course should include practical examples related to data bases, real world scenarios and introduce hands-on experience with both RDBMS (MySQL/PostgreSQL) and NoSQL (MongoDB, Firebase).
Operating Systems	Integrate hands-on labs and projects using virtualization tools and modern operating system platforms to reinforce core concepts. Cover current topics like process management, concurrency, security, and distributed systems, emphasizing real-world applications and collaborative learning
Artificial Intelligence	No Modifications Required
Database Management Systems Lab	Integrate hands-on projects using industry-standard database tools and real-world datasets, emphasizing practical skills in data modeling, querying, transaction management, and collaborative problem-solving
Operating Systems Lab	Incorporate practical assignments using contemporary OS tools and virtualization technologies, focusing on hands-on experience with process management, file systems, and system-level troubleshooting.
Artificial Intelligence Lab	No Modifications Required
Full Stack Development-I	Incorporate hands-on projects using modern frameworks, collaborative coding practices, and real-world application development to strengthen both front-end and back-end skills in a full stack environment.
Environmental Science	The syllabus can be improved by including topics like climate change, waste-to-energy, and SDGs for better relevance. Field-based learning through environmental surveys, pollution monitoring, and case studies will enhance practical exposure. Interactive activities like debates, role plays, and presentations will encourage active learning and environmental awareness.



Dr. A. Ganesh

HOD

Computer Science and Engineering

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